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751

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10050002	FILING DATE 01/15/2002	CLASS 257	SUBCLASS 070	GAU 2841	EXAMINER 211
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**APPLICANTS: Pavier Mark;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/262,890 01/19/2001

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no
35 USC 119 conditions met ☐ yes ☐ no
Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

IR-1837

TITLE : Back-to -back connected power semiconductor device package

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED			
		Assistant Examiner	<table border="1"> <tr> <td>Total Claims</td> <td>Print Claim for O.G.</td> </tr> </table>	Total Claims	Print Claim for O.G.
Total Claims	Print Claim for O.G.				
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		<table border="1"> <tr> <td>Sheets Drwg.</td> <td>Figs. Drwg.</td> <td>Print Fig.</td> </tr> </table>	Sheets Drwg.	Figs. Drwg.
Sheets Drwg.	Figs. Drwg.	Print Fig.			
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED F R ISSUE	Application Examiner		
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